



KPR Institute of Engineering
and Technology

(Autonomous)

Avinashi Road, Arasur, Coimbatore - 641 407

**Department of Electronics and Communication Engineering
(Accredited by NBA)**

NEWSLETTER - MARCH 2023 ISSUE



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➤ **Vision**

To be a centre of excellence in the emerging areas of Electronics and Communication Engineering through research, innovation and value based education

➤ **Mission**

- To develop competency in emerging technologies through knowledge and skill based education
- To provide conducive environment for research and innovation to cater the societal needs
- To inculcate moral and ethical values to become socially responsible engineers

➤ **Programme Educational Objectives**

- **PEO 1:** Apply Electronics and Communication Engineering principles to develop socially and environmentally acceptable engineering solutions.
- **PEO 2:** Embrace technological challenges through skill upgradation or higher education or research.
- **PEO 3:** Exhibit leadership qualities with professional and ethical values.

EVENT ORGANISED

- The VAC on Design, Simulation and Analysis of Antenna using CST Microwave Studio Suite was conducted by Dr.V.Seethalakshmi, Dr.S.Nithya, Mr.M.Singaram and R.Sathish Kumar. Various antenna designs were developed under different frequency range and for different applications and simulation of antennas using CST were done. CST Studio Suite antenna design covers a wide range of industries. Public broadcasting (TV/Radio), military, aerospace, and nautical industries have been using antennas for decades. More recently, the emergence of antennas in devices in our homes and handheld, smart devices. Oftentimes these devices contain several different antennas for communications at different frequencies. The most obvious example is a smartphone. In a smartphone, antennas are required for the 4 or 5G mobile network for the standard talk and internet connectivity. We have a Bluetooth antenna for accessories such as a speaker or headphones. There is a WiFi antenna, for internet connectivity at home or in the office and an NFC antenna that allows us to pay with a tap of our phone. Because of our “smart” or “connected” devices, many more companies across all industries are using EM simulation to analyse the performance of the antennas required to communicate with the world.

The poster is for a course titled "Design, Simulation and Analysis of Antenna using CST Microwave Studio Suite". It features logos for KPR Institute of Engineering and Technology, Department of Electronics and Communication Engineering, and Great Place To Work Certified. The course is coordinated by Dr. V. Seethalakshmi and Dr. S. Nithya. Resource persons include Dr. V. Seethalakshmi, Mr. N. Sathishkumar, Dr. S. Nithya, and Mr. M. Singaram. The course is held on 24.03.2023 to 27.03.2023, from 08.45 AM to 04.15 PM, at the MBC Lab. Social media links for eceKPRIET and KPRIETonline are provided at the bottom.

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Design, Simulation and Analysis of Antenna using CST Microwave Studio Suite

Resource Persons

 Dr.V.Seethalakshmi Professor/ECE, KPRIET, Coimbatore.	 Mr.N.Sathishkumar AP(Sr.G)/ECE, KPRIET, Coimbatore.
 Dr.S.Nithya AP(Sr.G)/ECE, KPRIET, Coimbatore.	 Mr.M.Singaram AP(Sr.G)/ECE, KPRIET, Coimbatore.

Coordinators:
Dr.V.Seethalakshmi,
Professor/ECE
Dr.S.Nithya,
AP(Sr.G)/ECE

Date : 24.03.2023 to 27.03.2023
Time : 08.45 AM to 04.15 PM
Venue : MBC Lab

eceKPRIET  KPRIETonline

- The Department of Electronics and Communication Engineering has organized a One Credit Course on “Embedded System Design for IoT Applications” as to inculcate the skills related to Embedded Systems for IoT applications in along with the Industry trainers (Mathiyazhgan R Managing Director and Vasantha Kumar M - Technical Lead) Rightway Technology, Coimbatore on 24.03.2023 to 25.03.2023. The resource person has taken the Embedded System Design through Hands-on Session in CC3200 Ti LaunchPad and NodeMCU Microcontrollers. The inputs of the resource person has kindled our students to get the hardware skills for their career and made them to ready for the placement drive.

The Department of Electronics and Communication Engineering has organized a One Credit Course on “Embedded System Design for IoT Applications” as to inculcate the skills related to Embedded

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Department of Electronics and Communication Engineering
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One credit course on Embedded System Design for IoT Applications

Resource Persons

Mr. R. Mathiyazhgan
Managing Director,
Rightway Technology,
Coimbatore.

Mr. M. Vasantha Kumar
Technical Lead -
Rightway Technology,
Coimbatore.

Coordinators:
Dr. D. Selvakumar,
AsP/ECE
Mr. Abhishek Sahu,
AP(SI.G)/ECE

Date : 24.03.2023 & 25.03.2023
Time : 08.45 AM to 04.15 PM
Venue : PSD Lab

eceKPRIET KPRIETonline

- The Value Added Course on ToT with RaspberryPi was conducted by Dr.M.Kalamani, Dr.R.Jaikumar and Mrs.S.priyadharsini. Along with coordinators, Dr.R.Arun Sekar and Mr.K.Saravanan has handled the sessions with hands on training in RaspberryPi. Overview on RaspberryPi 3 B model and its pin descriptions provided in the introduction session followed by hands on sessions in all basic sensor module interfacing with Pi and cloud. Finally, accessing of Raspberry Pi Wi-Fi, Bluetooth and MQTT protocols used for home automation and industrial applications are demonstrated with hands on training.

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Department of Electronics and Communication Engineering
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Value added course on IoT with Raspberry Pi

Resource Persons

Dr. M. Kalamani,
Professor/ECE,
KPRIET, Coimbatore.

Ms. K. Saravanan,
AP(Sr.G)/ECE,
KPRIET, Coimbatore.

Dr. R. Arun Sekar,
AsP/ECE,
KPRIET, Coimbatore.

Ms. S. Priyadharsini,
AP(Sr.G)/ECE,
KPRIET, Coimbatore.

Dr. R. Jaikumar,
AsP/ECE,
KPRIET, Coimbatore.

Coordinators:
Dr. M. Kalamani
Professor/ECE
Dr. R. Jaikumar,
AsP/ECE

Date : 27.03.2023 to 30.03.2023
Time : 08.45 AM to 04.15 PM
Venue : AMI Centre

eceKPRIET KPRIETonline

- PCB design using Altium Industrial one-credit course was organized by the ECE department in PCB Laboratory from 29.03.2023 to 1.4.2023. Around 29 students attended the program. The session was handled by Mr. Chandru T, Managing Director, Powerline Circuits, and Dr. Finney Daniel Shadrach, AP (SL.G) / ECE. The session was divided into two parts, namely design and reverse engineering. PCB design helps in bringing electronic circuits to life in their physical form. In the design part students were exposed to the various PCB design processes involved in Altium software such as schematics, schematic library, footprint generation, PCB library, PCB layout, and Gerber generation. Further, students were also given awareness about the various open-source PCB software available in the market such as Kicad, Eagle, Allegro etc.. In the second part, students were given exposure to PCB board cloning. Students felt interested and the participants were also explained about the SDG and were able to justify it in connection to PCB. At the end of the session, students were able to design a PCB board.

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One credit course on PCB Design using Altium

Resource Persons

Mr. Chandru Thandabani
Managing Director,
Powerline Circuits,
Coimbatore.

Dr. S. Finney Daniel
Assistant Professor(SI.G),
KPRIET,
Coimbatore.

Coordinator:
Dr. S. Finney Daniel,
AP(SI.G)/ECE

G2

Date : 29.03.2023 & 31.03.2023
Time : 08.45 AM to 04.15 PM
Venue : PCB Lab

eceKPRIET f in t y KPRIETonline

- The one credit course training on Software Defined Radio is handled by R. Balamuruges, CEO, Silicon Systems and Mr S Raja, Product Development Manager, Silicon Systems, Kalapatti, Coimbatore-641 048. The event started at 8.45am and ends at 4.15pm.. Software-defined radio (SDR) is a radio communication system where components that have been traditionally implemented in analog hardware (e.g. mixers, filters, amplifiers, modulators/demodulators, detectors, etc.) are instead implemented by means of software on a personal computer or embedded system. A basic SDR system may consist of a personal computer equipped with a sound card, or other analog-to-digital converter, preceded by some form of RF front end. Such a design produces a radio which can receive and transmit widely

different radio protocols (sometimes referred to as waveforms) based solely on the software used. Students were very much interactive throughout the session and completed the project assigned to them

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Department of Electronics and Communication Engineering
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MAR 2022 - MAR 2023

One credit course on Software defined Radio

Coordinators:
Dr.V.Seethalakshmi, Professor/ECE
Dr.S.Nithya, AP(Sr.G)/ECE

Resource Persons

Mr.R.Balamuruges
CEO, Silicon Systems, Kalapatti, Coimbatore.

Mr.S.Raja
Product Development Manager, Silicon Systems, Kalapatti, Coimbatore.

Date : 24.03.2023 & 25.03.2023
Time : 08.45 AM to 04.15 PM
Venue : ADC Lab

eceKPRIET KPRIETonline

- The Department of Electronics and Communication Engineering is organized a Value Added Course on Signal and Image Processing using MATLAB tools from 28.03.2023 to 31.03.2023. Forty students from II ECE attended this value added course. Hands on training sessions were conducted. Sample MATLAB codes were explained and students are instructed to practice more. Students felt very useful in attending this value added course. Students were informed to take some real time applications of MATLAB and work on it.

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Department of Electronics and Communication Engineering
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MAR 2022 - MAR 2023

Value added course on Signal and Image analysis using MATLAB tools

Coordinators:
Dr.K.Kalirajan, Professor/ECE
Dr.S.Finney Daniel, AP(Sr.G)/ECE, KPRIET, Coimbatore.

Resource Persons

Mr.G.Pradeepkumar
AP(Sr.G)/ECE, KPRIET, Coimbatore.

Mr.S.Satheesh Kumar
AP(Sr.G)/ECE, KPRIET, Coimbatore.

Mr.D.Ram Nivas
AP/ECE, KPRIET, Coimbatore.

Date : 28.03.2023 to 31.03.2023
Time : 08.45 AM to 04.15 PM

eceKPRIET KPRIETonline

- In the past two decades, CMOS technology has rapidly embraced the field of analog integrated circuits, providing low cost, high performance solutions and rising to dominate the market. The CMOS processes have emerged as a viable choice for the integration of today's complex mixed signal and SOC systems. Analog circuit design has evolved with the technology moving from high voltage, high power small scale analog circuits to low voltage, low power ultra large scale circuits. Analog IC design generally involves more personalized focus into each circuit, and even the sizing and specifics of each transistor. Also, many foundry processes are primarily developed for digital ICs with analog features, which requires analog IC designers to work with process constraints and features better suited to digital ICs.
- Cadence circuit design solutions enable fast and accurate entry of design concepts, which includes managing design intent in a way that flows naturally in the schematic. The value added course on Analog IC design using CADENCE is started with introduction to analog circuits. The operation and characteristics of the analog circuits were taught during the first day of the course. Later on the CADENCE EDA tool demonstration was carried for basic circuits to make the students understand the usage of the tool. The next day the students are instructed to design fundamental circuits using CADENCE EDA tools to have a hands on training. Then various performance analyzing parameters were taught in the post students hands on session. The assessment is carried out for the students based on the learning carried out in the course.



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Value added course on

Analog IC Design using Cadence



Resource Persons



Dr. J. Muralidharan

AsP/ECE,
KPRIET, Coimbatore.



Mr. Abhishek Sahu

AP(SI.G)/ECE,
KPRIET, Coimbatore.



Dr. J. Prasad

AP(SI.G)/ECE,
KPRIET, Coimbatore.



Mr. D. Ram Nivas

AP(Sr.G)/ECE,
KPRIET, Coimbatore.



Ms. M. D. Saranya

AP/ECE,
KPRIET, Coimbatore.

Coordinators:

Dr. J. Muralidharan,
AsP/ECE
Dr. J. Prasad,
AP(SI.G)/ECE

Date : 23.03.2023 to 25.03.2023

Time : 08.45 AM to 04.15 PM

Venue : VLSI Lab

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Icmr

In the past two decades, CMOS technology has rapidly embraced the field of analog integrated circuits, providing low cost, high performance solutions and rising to dominate the market. The CMOS processes have emerged as a viable choice for the integration of today's complex mixed signal and SOC systems. Analog circuit design has evolved with the technology moving from high voltage, high power small scale analog circuits to low voltage, low power ultra large scale circuits. Analog IC design generally involves more personalized focus into each circuit, and even the sizing and specifics of each transistor. Also, many foundry processes are primarily developed for digital ICs with analog features, which requires analog IC designers to work with process constraints and features better suited to digital ICs. Cadence circuit design solutions enable fast and accurate entry of design concepts, which includes managing design intent in a way that flows naturally in the schematic. The value added course on Analog IC design using CADENCE is started with introduction to analog circuits. The operation and characteristics of the analog circuits were taught during the first day of the course. Later on the CADENCE EDA tool demonstration was carried for basic circuits to make the students understand the usage of the tool. The next day the students are instructed to design fundamental circuits using CADENCE EDA tools to have a hands on training. Then various performance analyzing parameters were taught in the post students hands on session. The assessment is carried out for the students based on the learning carried out in the course.

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Alumni Lecture Series - 4
Insights on Higher Education in Overseas

Resource Person

Mr. M. Vaishnav
(2017-2021)
Pursuing M.Sc.(CNSS),
University of Hertfordshire,
Hatfield, UK.

Coordinators:
Dr. B. Jaishankar,
Professor/ECE
Ms. M. Supriya,
AP/ECE

Date : 25.03.2023
Time : 10.30 AM to 11.30 AM
Venue : II ECE B Classroom

G2
WSE 2023 NSMA

eceKPRIET KPRIETonline

- The online webinar started with the welcome note by Dr. R Arun sekar, following the welcome address, a brief information regarding the Symica tool was explained by Amit Saini, Director and Technical Head of Cadre design systems. Later he delivers a comprehensive set of EDA tools that enable companies around the world to design analog and mixed-signal integrated circuits with full flow. Further, he explained the major capabilities of modern IC development suites. Easy accommodation of different Process Development Kits (PDK) as well as affordable and flexible Symica an attractive solution for startups and independent researchers. Mid-size and well-established semiconductor companies can and Symica is a cost-saving solution for expanding their design forces in addition to limit license sets from major EDA vendors. Moreover, Complete solution for analog and mixed-signal IC designer is explained by him. Then narrated about the Accurate circuit simulation, Analog, behavioral and digital mixed-mode simulation. Industry compatible: easy import-export of entire design. Silicon proven Competitive and flexible pricing. He also explored the SymSpice which is an original SPICE circuit simulator completely developed by the Symica R&D team. SymSpice provides simulation speed and accuracy at the "golden" SPICE level. SymSpice supports all commonly used transistor models and is compatible with industry-standard netlist formats.


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 2022-2023

Layout and Analysis of CMOS circuits using Symica Tool


Coordinators:
 Dr.M.Kathirvelu, Professor /ECE
 Dr.R.Arun Sekar, AsP/ ECE
 Dr.J.Muralidharan, AsP/ECE


Resource Person
Mr.Amit Saini
 Director and Tech head
 Cadre Design Systems
 Uttar Pradesh.


Date : 14.03.2023
Time : 02.00 PM to 04.00 PM
Venue : Thanam Hall

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The online webinar started with the welcome note by Dr. R Arun sekar, following the welcome address, a brief information regarding the Symica tool was explained by Amit Saini, Director and Technical Head of Cadre design systems. Later he delivers a comprehensive set of EDA tools that enable companies around the world to design analog and mixed-signal integrated circuits with full flow. Further, he explained the major capabilities of modern IC development suites. Easy accommodation of different Process Development Kits (PDK) as well as affordable and flexible pricing that makes Symica an attractive solution for startups/Academic Institutes and independent researchers. He explained the major capabilities of modern IC development suites. Easy accommodation of different Process Development Kits (PDK) as well as affordable pricing makes that Symica an attractive solution for startups and independent researchers. Mid-size and well-established semiconductor companies can and Symica is a cost-saving solution for expanding their design forces in addition to limit license sets from major EDA vendors. Moreover, Complete solution for analog and mixed-signal IC designer is explained by him. Then narrated about the Accurate circuit simulation, Analog, behavioral and digital mixed-mode simulation. Industry compatible: easy import-export of entire design. Silicon proven Competitive and flexible pricing. He also explored the SymSpice which is an original SPICE circuit simulator completely developed by the Symica R&D team. SymSpice provides simulation speed and accuracy at the "golden" SPICE level. SymSpice supports all commonly used transistor models and is compatible with industry-standard netlist formats. Finally, he demonstrated the SymSpice Turbo specially designed to work with very large scale integrated circuits and system on chips. In recent years several Fast-Spice tools have appeared on the market and they trade speed for accuracy. Symica's proprietary math methods provide accurate Fast-Spice simulation maintaining accuracy at "golden" SPICE level. Simulation time reduction factor is highly dependent on the type of design and in "typical" designs involving several thousand transistors it is about 3 to 10 and in an exceptional case we can find a factor up to 1,000.

ABOUT THE COLLEGE

KPR Institute of Engineering and Technology (KPRIET), is a new generation industry driven engineering college established in the year 2009. It is run by KPR Group, a leading corporate house under the dynamic leadership of great visionary chairman Shri K.P.Ramasamy. It is located on sprawling 150 acres of lush green campus with 7.5 Lakh sq.ft buildings of architectural excellence. The Institute has eight centers of excellence. The institute has been approved by All India Council for Technical Education (AICTE), New Delhi, accredited by NBA, NAAC with 'A' Grade and affiliated to Anna University, Chennai. The institute offers 12 Under Graduate, 4 Post Graduate programmes in Engineering & Technology, and 9 Ph.D. programmes.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering came into existence in the year 2009. It offers a UG programme, B.E. in Electronics and Communication Engineering accredited by NBA and a PG programme, M.E.VLSI Design. The department has a team of committed faculty members who are well qualified and are backed by rich teaching/research/industry experience. The growing needs and aspirations are addressed by the department to provide additional opportunities for younger generation to meet industrial scenario. The department has published 270+ articles, 60+ patents in the last three years and received the funding proposal grant of Rs. 73 lakhs. The department has 10 curriculum-based laboratories equipped with sophisticated software, Center of excellence in embedded systems & IoT and Antenna with leading industries.

ABOUT THE WORKSHOP

The workshop will contribute to the Clinical/Translational Research on "Integrated Care Model for Mental and Neurological Disorders" in order to educate, train, and inspire people in the biomedical field. This will address new challenges in examination that includes Biopsy, Diagnostic testing, Imaging tests, Laboratory tests, Mental function tests and Neurological exam.

- This workshop is intended to shed some light on recent advances in diagnosing neurological disorders and identify further research areas in this exciting field of Artificial Intelligence.
- The workshop will highlight current and new approaches, systems and technologies to the treatment of brain diseases, including diagnosis, rehabilitation, theoretical and practical elements.
- An integrated care model encompasses the whole care process, from prodromal, early diagnosis to disease management, treatment and patient empowerment.

DHR-ICMR Sponsored



Two days National level Workshop
on
Clinical Training/Translational
Research on Integrated Care Model
for Mental and Neurological Disorders

10.03.23 & 11.03.23

Organized by



Department of Electronics and Communication
Engineering



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Technology
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Coimbatore-641407.
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FACULTY PUBLICATIONS

1. **M. Supriya**, V. Jayaraj, S. Vinod, V. Uma Maheswari, M. A. Raja and S. Krishnakumar, "RFLSR based Productive MBIST Architecture for VLSI," 2022 Second International Conference on Advanced Technologies in Intelligent Control, Environment, Computing & Communication Engineering (ICATIECE), Bangalore, India, 2022, pp. 1-5, doi: 10.1109/ICATIECE56365.2022.10047528.
2. **T. Jagadesh**, A. Reethika, B.Jaishankar, & M.S.Kanivarshini. (2023). Early prediction of epileptic seizure using Deep Learning algorithm. *Brain-Computer Interface*, 157–177. <https://doi.org/10.1002/9781119857655.ch7>
3. **Vasim Babu**, Sekaran, R., Kannan, S., & Ravi, V. (2023). CE2RV: Commissioned energy-efficient virtualization for large-scale heterogeneous wireless sensor networks. *International Journal of Communication Systems*. <https://doi.org/10.1002/dac.5480>
4. Arumugam, C., Kandasamy, S. K., & **K.S.Tamilselvan** (2023). Enhancing supercapacitor performance using zno embedded on go/PPY composite as versatile electrodes. *High Energy Chemistry*, 57(1), 69–76. <https://doi.org/10.1134/s0018143923010034>
5. S. K. Agarwal, **S. M. Ramesh**, A. A. Kumar, S. Yadav, M. Nagalakshmi and P. Singh, "A microservices-based IoT applications in Edge computing environments," 2022 2nd International Conference on Innovative Sustainable Computational Technologies (CISCT), Dehradun, India, 2022, pp. 1-5, doi: 10.1109/CISCT55310.2022.10046492
6. **Arun Sekar R**, Kirubalini, "Design of arithmetic circuits using an Approximation computing", IEEE International conference on Advanced computing and computing systems, 17-18th march 2013
7. **Muralidharan J**, "Cybersecurity in IoT using deep learning models", 2nd International conference on sustainable computing and data communication systems, 23-25 march 2023
8. **K.Kalirajan**, "Alzheimer's Disease classification using convolutional neural networks", IEEE International conference on Innovative data communication technologies and application, 14-15 march 2023
9. **M.D. Saranya**, "Deep neural networks for standard value estimation", Third international conference on Artificial Intelligence and smart computing" 9th to 11th march 2023
10. Vidhya, N., **Seethalakshmi, V.**, Suganyadevi, S. (2023). Non-silicon Computing with Quantum Superposition Entanglement Using Qubits. In: Pandey, R., Srivastava, N., Singh, N.K., Tyagi, K. (eds) Quantum Computing: A

FUNDING DETAILS

- The Funding received from ICMR for the title of “Clinical Training/Translational Research on Integrated Care Model for Mental and Neurological Disorders “ the funding sanction amount is Rs 7,44,000/-.

FACULTY AS RESOURCE SPEAKER

- Dr.V.Seethalaskhmi, Professor, Dr.S.Nithya, Assistant professor (Sr.G), Mr.N.Sathish kumar, Assistant professor (Sr.G), Mr.M.Singaram, Assistant professor (Sr.G) Department of ECE, KPR Institute of Engineering and Technology as resource person for value added course “Design ,Simulation, and analysis of antenna using CST microwave studio suite “ organised by KPR Institute of Engineering and Technology on 24.03.2023 to 27.03.2023.

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Design, Simulation and Analysis of Antenna using CST Microwave Studio Suite

Resource Persons

 Dr.V.Seethalakshmi Professor/ECE, KPRIET, Coimbatore.	 Mr.N.Sathishkumar AP(Sr.G)/ECE, KPRIET, Coimbatore.
 Dr.S.Nithya AP(Sr.G)/ECE, KPRIET, Coimbatore.	 Mr.M.Singaram AP(Sr.G)/ECE, KPRIET, Coimbatore.

Coordinators:
Dr.V.Seethalakshmi, Professor/ECE
Dr.S.Nithya, AP(Sr.G)/ECE

Date : 24.03.2023 to 27.03.2023
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- Dr.J.Muralidharan, Associate Professor, Dr.J.Prasad, Assistant professor (Sl.G), Mr.Abhishek Sahu , Assistant professor (Sl.G), Mr.D.Ram Nivas, Assistant professor (Sr.G), Ms.M.D.Saranya, Assistant professor Department of ECE, KPR Institute of Engineering and Technology as resource person for value added course “Analog IC design using Cadence “ organised by KPR Institute of Engineering and Technology on 23.03.2023 to 27.03.2023.


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Great Place To Work Certified
 MAR 2022 - MAR 2023

Value added course on
Analog IC Design using Cadence




Resource Persons

 Dr.J.Muralidharan AsP/ECE, KPRIET, Coimbatore.	 Mr.Abhishek Sahu AP(SI.G)/ECE, KPRIET, Coimbatore.
 Dr.J.Prasad AP(SI.G)/ECE, KPRIET, Coimbatore.	 Mr.D.Ram Nivas AP(Sr.G)/ECE, KPRIET, Coimbatore.
	 Ms.M.D.Saranya AP/ECE, KPRIET, Coimbatore.


Coordinators:
Dr.J.Muralidharan,
 AsP/ECE
Dr.J.Prasad,
 AP(SI.G)/ECE
 eceKPRIET


Date : 23.03.2023 to 27.03.2023
 Time : 08.45 AM to 04.15 PM
 Venue : VLSI Lab



 KPRIETonline






- Dr.K.Kalirajan, Professor, Dr.K.Murugan, Associate professor ,Dr.S.Finney Daniel Shadrach ,Assisatnt professor (Sr.G), Mr.G.Pradaeep kumar, Assisatnt professor (Sr.G),Mr.S.Satheesh kumar,Assisatnt professor (Sr.G), Mr.D.Ram Nivas,Assistant professor (Sr.G), Department of ECE, KPR Institute of Engineering and Technology as resource person for value added course “Signal and Image analysis using MATLAB tools “ organised by KPR Institute of Engineering and Technology on 28.03.2023 to 31.03.2023.


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Great Place To Work Certified
 MAR 2022 - MAR 2023

Value added course on
Signal and Image analysis using MATLAB tools







Resource Persons

 Dr.K.Kalirajan Professor/ECE, KPRIET, Coimbatore.	 Mr.G.Pradeepkumar AP(Sr.G)/ECE, KPRIET, Coimbatore.
 Dr.K.Murugan AsP/ECE, KPRIET, Coimbatore.	 Mr.S.Satheesh Kumar AP(Sr.G)/ECE, KPRIET, Coimbatore.
 Dr.S.Finney Daniel AP(Sr.G)/ECE, KPRIET, Coimbatore.	 Mr.D.Ram Nivas AP/ECE, KPRIET, Coimbatore.

Coordinators:
Dr.K.Kalirajan,
 Professor/ECE
Dr.K.Murugan,
 AsP/ECE

Date : 28.03.2023 to 31.03.2023
 Time : 08.45 AM to 04.15 PM
 Venue : VLSI Lab


 KPRIETonline

- Dr.S.Finnay Daniel Shadrach,Assistant professor (Sl.G) Department of ECE, KPR Institute of Engineering and Technology as resource person for One credit course “PCB Design using altium“ organised by KPR Institute of Engineering and Technology on 29.03.2023 & 31.03.2023.

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Department of Electronics and Communication Engineering
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Great Place To Work Certified

One credit course on PCB Design using Altium

Resource Persons

Mr.Chandru Thandabani
Managing Director, Powerline Circuits, Coimbatore.

Dr.S.Finney Daniel
Assistant Professor(Sl.G), KPRIET, Coimbatore.

Coordinator:
Dr.S.Finney Daniel,
AP(Sl.G)/ECE

Date : 29.03.2023 & 31.03.2023
Time : 08.45 AM to 04.15 PM
Venue : PCB Lab

eceKPRIET KPRIETonline

- Dr.M.Vasim babu, Professor, Dr.R.Gurumoorthi,Associate Professor, Department of ECE, KPR Institute of Engineering and Technology as resource person for hands on session “Innovation in wireless communication and security“ organised by PSNA college of Engineering and Technology on 07.03.2023.

PSNA COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution)
Estd - 1984 AICTE | Anna University | NBA | NAAC A++

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Hands-on Session on Innovation in Wireless Communication and Security

7TH MARCH

Registration Fees
Students/Faculty:
Rs.300/-

For Registration
<https://tinyurl.com/5n8fh9vh>

CONVENOR
Dr.G.Athisha
HOD/ECE

Resource Persons

Dr M.Vasim babu
Professor
Department of ECE
KPR Institute of Engineering and Technology
Coimbatore.

Dr K.B Gurumoorthy
Associate Professor
Department of ECE
KPR Institute of Engineering and Technology
Coimbatore.

COORDINATORS
Dr.J.Kumarnath AP/ECE
Mr.P.Rajasekaran AP/ECE

CO-COORDINATORS
Dr.R.Mohan Raj AP/ECE
Mrs.D.Gayathri AP/ECE

Robolab Technologies, LabVIEW, TIT, TESSOLVE, KEYSIGHT TECHNOLOGIES, E-CAP, TEXAS INSTRUMENTS

FACULTY PARTICIPATION

Name of the Faculty	Title of FDP/STTP/OFPD/Conference/Online Course	Organization Name	Start Date	End Date
Ms.S.Nithya	Design of a Robotic Helpmate	Sri Eshwar College of Engineering	17-03-2023	17-03-2023
Ms.S.Gunanandhini	SCUBA Divers Health Monitoring and Risk Management System	Sri Eshwar College of Engineering	17-03-2023	17-03-2023
Ms.MD.Saranya	Deep Neural Networks for Standard Value Estimation	Bannari Amman Institute of Technology	11-03-2023	11-03-2023
Ms.S.Suganyadevi	Alzheimer's Disease Diagnosis using Deep Learning Approach	St. Mother Theresa Engineering College	02-03-2023	02-03-2023
Mr.G.Pradeepkumar	Secure Multimodal Biometric SystemBased on Robust LSB-DWT Digital Watermarking Algorithm	LBS Institute of Technology for Women	03-03-2023	03-03-2023
Mr.G.Pradeepkumar	Sports Applications of Biomechanics Wearable Sensors using IoT	St. Mother Theresa Engineering College	02-03-2023	02-03-2023
Mr.G.Pradeepkumar	An IoT based Low Cost E-Parking System in Smart Cities	St. Mother Theresa Engineering College	04-03-2023	04-03-2023
K.Kalirajan	Alzheimer's Disease classification using convolutional neural networks	IEEE International conference on Innovative data communication technologies and application	14-03-2023	15-03-2023
Muralidharan J	Cybersecurity in IoT using deep learning models	2 nd International conference on sustainable computing and data communication systems	23.03.2023	25.03.2023
Arun Sekar R	Design of arithmetic circuits using an Approximation	IEEE International conference on Advanced computing	17.03.2023	18.03.2023

	computing	and commuting systems		
Dr.J.Prasad	Disruptive Technologies in Electronics, Communication and Signal Processing	Sri Ramakrishna Institute of Technology, Coimbatore	20-03-2023	24-03-2023
Mr.G.Pradeepkumar	Clinical Training / Translational Research on Integrated Care model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Ms.S.Nithya	DHR- clinical Two days National Level workshop on Clinical Training/ Translational Research on Integrated Care Model for Mental and Neurological disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Mr.T.Jagadesh	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Ms.M.Supriya	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Dr.K.Murugan	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Mr.T.Venkatesh	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Mr. S. Balamurali	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023

Ms.S.Suganyadevi	Clinical Training / Translational Research on Integrated Care Model for Mental and Neurological Disorders	KPR Institute of Engineering and Technology	10-03-2023	11-03-2023
Dr.M.Kalamani	Introduction to Internet of Things and Embedded Systems	Coursera	05-03-2023	05-03-2023
Ms.S.Gunanandhini	Communication skills, modes and knowledge Dissemination	NITTT	01-11-2022	31-01-2023
Ms.S.Gunanandhini	Institutional management and administrative procedures	NITTTR	01-11-2022	31-01-2023
Ms.S.Gunanandhini	Design of high frequency antennas for real time application	GMR institute of technology and V R Siddhartha Engineering College	14.03.2023	18.03.2023

PLACEMENT ACTIVITIES

S.NO	NAME OF THE STUDENT	YEAR	COMPANY
1	KARTHIK K	IV	Q SPIDER
2	INBARAJ	IV	Q SPIDER
3	SUGAIBURAHMAN	IV	Q SPIDER
4	LINGAMOORTHY	IV	ADECCO
5	GOKUL PRASATH	IV	ADECCO
6	ADECCO	IV	ADECCO

STUDENT PARTICIPATION

S.No	Event Name	No of students participated
1	Online course	2
2	workshop	3
3	Contest (Quiz, Coding Contest, club events, etc.)	11
4	Paper presentation	4
5	Internship	Nil
6	Project Presentation	4
7	NSS, YRC activities	Nil

STUDENT ACHIEVEMENT

S.No	Name of the Student	Year	Title of the Event	Organized by (Name of College and Club)	Prize - Merit Position
1.	VARUN PR	III YEA R C	TECHNEX'23 - HOVERCRAFT (18.03.2023)	IIT, VARANASI	III
2.	VARUN PR	III YEA R C	TECHNEX'23 - DRONETE H (18.03.2023)	IIT, VARANASI	III
3.	LALITH	III ECE B	DRONE RACING	IIT, VARANASI	I
4.	MADHAVAN	III ECE B	DRONE RACING	IIT, VARANASI	II
5.	LALITH	III YEA R B	TECHNEX'23 - HOVERCRAFT (18.03.2023)	IIT, VARANASI	I
6.	MADHAVAN	III ECE B	TECHNEX'23 - HOVERCRAFT (18.03.2023)	IIT, VARANASI	II
7.	MADHAVAN	III ECE B	DRONE RACING	IIT, ROPAR	II

8.	MADHAVAN	III ECE B	ZEITGEIST'23 - HOVERCRAFT (23.03.2023)	IIT, ROPAR	III
9.	LALITH	III YEA R B	DRONE RACING	IIT, ROPAR	I
10	LALITH	III YEA R B	ZEITGEIST'23 - HOVERCRAFT (23.03.2023)	IIT, ROPAR	I
11	VARUN PR	III YEA R C	ZEITGEIST'23 - HOVERCRAFT (23.03.2023)	IIT, ROPAR	II
12	VARUN PR	III YEA R C	TECHKRITI 2023 - HOVERDRIVE	IIT, KANPUR	III
13	VARUN PR	III YEA R C	TECHKRITI 2023 - MULTIROTOR	IIT, KANPUR	II
14	VARUN PR	III ECE C	AERIAL DASH	SRI RAMAKRISHNA COLLEGE OF TECHNOLOGY	III
15	LAKSHNA SHURUTHI	III ECE B	PAPER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	II
16	MANOJ. M	III ECE B	PAPER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	II
17	MOHANRAJ	III ECE B	PAPER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	II
18	PREETI. E. S	III ECE B	PAPER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	II
19	LIVYASHREE	III ECE	PAPER PRESENTATIO	KALAINAR KARUNANIDHI	II

	M	B	N	INSTITUTE OF TECHNOLOGY	
20	PREETI. E. S	III ECE B	POSTER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	III
21	LIVYASHREE	III ECE B	POSTER PRESENTATIO N	KALAINAR KARUNANIDHI INSTITUTE OF TECHNOLGY	III
22	LALITH	III ECE B	MEGA TECO CULTURAL SPORTS FEST	KUMARAGURU COLLEGE OF TECHNOLOGY	II



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1 TEAM 3 IITs



LALITH K
ECE 3rd Year



MADHAVAN S
ECE 3rd Year



VARUN P R
ECE 3rd Year

MENTORED BY
Dr. ARIVAZHAGAN S
Innovation officer



IIT VARANASI
TECHNEX 23



DRONE TECH

(Drone Race)

Badged all 3 Prizes

AEROGLISSEUR

(Hover Craft)

Badged all 3 Prizes

CASH PRIZE WORTH ₹13 L

IIT KANPUR
TECHKRITI 23



MULTIROTOR

(Drone Race)

Badged all 3 Prizes

HOVERMANIA

(Hover Craft)

Badged all 3 Prizes

CASH PRIZE ₹3.5 L

IIT ROPAR
ZEITGEIST 23



MISSION RAKSHAK

(Drone Race)

Badged all 3 Prizes

HOVERCRAFT

(Hover Craft Race)

Badged all 3 Prizes

CASH PRIZE ₹90 K

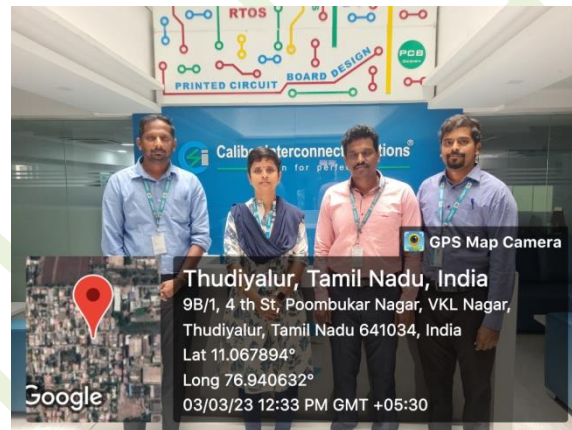
@KPR_ARC #NAMMAKPR



IIPC ACTIVITIES

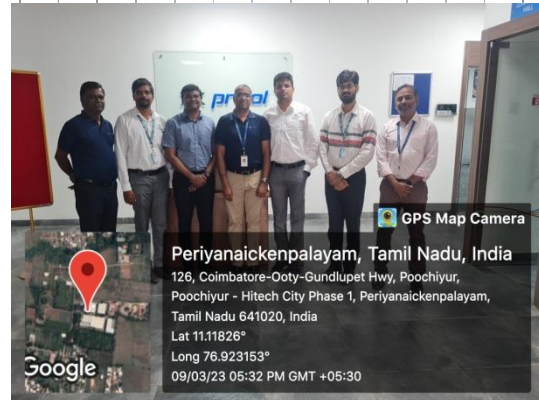
INDUSTRY VISITS

- Dr. J. Indra, Dr.R. Jaikumar, Mr.T. Jagadesh and Mr.T. Venkatesh visited Caliber Interconnect Solutions on 03/03/2023 and discussed regarding core placements and futuristic activities in MoU.
- Mr.T. Jagadesh visited ElectraEV on 06/03/2023 and 4 students from 2019 – 2023 batch joined as intern with stipend of Rs.15,000.
- Mr.T. Jagadesh visited Pricol Engineering Industries on 09/03/2023 and discussed MoU and its outcomes.
- Dr.S. Finney Daniel visited Switching Avo Electro Power Limited on 08/03/2023 and discussed about placement through internship.
- Dr.M. Kalamani, Dr.R. Jaikumar, Dr.S. Finney Daniel, Ms.S. Priyadharsini visited V-Guard Industries and discussed about various consultancy projects.
- Dr.S. Finney Daniel and Mr.T. Jagadesh visited Universal Circuits and discussed about consultancy activities.
- Dr.B.Jaishankar and Mr.T. Jagadesh visited Enthu Technology Solutions regarding quotation for PCB fully automatic machine.



CONSULTANCY ACTIVITIES

- Dr.M. Kalamani completed consultancy project from V-Guard Industries and received Rs.24,600.



- Dr.K.S. Tamilselvan, Mr.G.K. Jakir Hussain and Mr.T. Venkatesh completed consultancy project from Enthu Technology Solutions Private Limited and received Rs.1,04,054.
- Dr.R. Kirubashankar completed consultancy project from Agastya Academy and received Rs.52,500.



EXPERT TALK

- Mr. Ramkrish, Mr.Mervin from Caliber Interconnect visited our institute on 11/03/2023 and interacted with III year ECE students regarding core placements.

